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Phon	e	Date:	1/26/2005				
Re:	U.S. Serial No: 09/855,241 Group Art Unit: 2183 Confirmation No: 8988 Docket No: YOR920000809US1 (14138)	CC:					
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CERTIFICATE OF TRANSMISSION BY FACSIMILE (37 CFR 1.8) Applicant(s): Vinodh R. Cuppa, et al. Docket No. YOR920000809US1 (141)									
Application No. 09/855,241	Filing Date May 15, 2001	Examiner Shane F. Gerstl	Group Art Unit 2183						
Invention: A METHOD A	Invention: A METHOD AND APPARATUS FOR REDUCING LOGIC ACTIVITY IN A MICROPROCESSOR								
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AMEND Applicant(s): Vi	arge En	tity)		Docket No. YOR920000809US1 (14138)				
Application No. 09/855,241	Filing Date May 15, 2001	Examiner Shane F. Gerst	1	Customer No. Group Art I 23389 2183		Group Art Unit 2183	it Confirmation No. 8988	
Invention: A METHOD AND APPARATUS FOR REDUCING LOGIC ACTIVITY IN A MICROPROCESSOR								
COMMISSIONER FOR PATENTS: Transmitted herewith is an amendment in the above-identified application. The fee has been calculated and is transmitted as shown below.								
	· · ·	CLAIMS AS AI	MENDED					
	CLAIMS REMAINING	HIGHEST #		R EXTRA		RATE	ADDITIONAL FEE	
TOTAL CLAIMS	20 -	20 =		0	x	\$50.00	\$0.00	
INDEP. CLAIMS	3 -	3 =		0	х	\$200.00	\$0.00	
Multiple Depender	nt Claims (check if app	licable)		,			\$0.00	
		TOTAL ADDITIONAL	FEE FOI	FEE FOR THIS AMENDMENT \$0.00				
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Steven Fischman Registration No. 3 SCULLY, SCOTT	the Uni	Dated: January 26, 2005 Thereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on						
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AMENDMENT TRANSMITTAL LETTER (Large Entity) Applicant(s): Vinodh R. Cuppa, et al.						Docket No. YOR920000809US1 (14138)		
Application No. 09/855,241			No.	Group Art Unit 2183	Confirmation No. 8988			
Invention: A METHOD AND APPARATUS FOR REDUCING LOGIC ACTIVITY IN A MICROPROCESSOR								
COMMISSIONER FOR PATENTS: Transmitted herewith is an amendment in the above-identified application.								
		mitted as shown below.	• •	on.				
	<u> </u>	CLAIMS AS AM	ENDED)				
	CLAIMS REMAINING	HIGHEST#	NUMBE	ER EXTRA			ADDITIONAL	
	AFTER AMENDMENT	PREV. PAID FOR	CLAIMS	PRESENT		RATE	FEE	
TOTAL CLAIMS	20 -	20 =		0	х	\$50.00	\$0.00	
INDEP. CLAIMS	3 -	3 =	0		х	\$200.00	\$0.00	
Multiple Dependen	t Claims (check if app	licable)					\$0.00	
		TOTAL ADDITIONAL F	EE FO	R THIS AM	END	DMENT	\$0.00	
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Applicants: Vinodh R. Cuppa, et al.

Examiner: Shane F. Gerstl

JAN 2 6 2005

Serial No.:

09/855,241

Art Unit: 2183

Filed: May 15, 2001

Docket: YOR920000809US1 (14138)

For: A METHOD AND APPARATUS FOR

TUS FUR

Confirmation No.: 8988

REDUCING LOGIC ACTIVITY IN A

Dated: January 26, 2005

MICROPROCESSOR

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AMENDMENT UNDER 37 CFR 1.111

Sir:

Responsive to the Office Action of October 26, 2004, Applicants respectfully request the Examiner to reconsider the application in view of the following amendments and remarks.

Amendments to the Abstract are reflected on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 3 of this paper.

Remarks begin on page 8 of this paper.

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